AMENDMENTS TO THE CLAIMS

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (currently amended) A computer-implemented method for using a processor as a virtual device, said method comprising:

loading data to a first location in a common memory in a computer system;

loading software code that processes the data in a second location in the common memory;

writing an instruction block to a third location in the common memory, wherein the instruction block includes the first and second locations;

signaling, from a first processor, a second processor from a plurality processors in [[a]] the computer system, wherein the plurality of processors share [[a]] the common memory, wherein each of the processors have a local memory, and wherein at least two of the processors are dislike;

storing data corresponding to the request in [[a]] the local memory corresponding to the second processor; and

in response to the signaling, reading by the second processor the second location from the instruction block;

copying, by the second processor, the data from the first location in the common memory to the second processor's local memory;

copying, by the second processor, the software code from the second location in the common memory to the second processor's local memory; and

processing the data by the second processor using the software code stored in the second processor's local memory.

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